

Notice of References Cited	Application/Control No. 09/754,406	Applicant(s)/Patent Under Reexamination XU, SONGJIE	
	Examiner Mary C Hogan	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Huang et al, "An Iterative Area/Performance Trade-Off Algorithm for LUT-Based FPGA Technology Mapping" Computer-Aided Design, 1996. ICCAD-96. Digest of Technical Papers., 1996 IEEE/ACM International Conference on , 10-14 Nov. 1996 Pages:13 - 17
	V	Singh et al, "A Heuristic Algorithm for the Fanout Problem", 27th ACM/IEEE Design Automation Conference, 1990, pages 357-360
	W	Ju et al "Incremental Techniques for the Identification of Statistically Sensitizable Critical Paths", 28th ACM/IEEE Design Automation Conference, Paper 32.2, pages 541-546, 1991
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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